

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1466	370/442	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:16
L2	1262	370/458	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:16
L3	1536	370/498	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:16
L4	3093	370/503	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:16
L5	503	370/508	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:16
L6	1191	370/516-519.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:17
L7	554	370/537.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:17
L8	40887	(adjust\$4 compensat\$6 determin\$4 control\$4 comput\$4 calculat\$4) with (phase jitter delay difference time) with (TDM TDMA (time adj slot) MUX multiplex\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:14
L9	1316	8 with filter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:19
L10	1449	8 with filter\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20
L11	11	10 and 1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:21
L12	7	10 and 2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20

L13	7	10 and 3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20
L14	20	10 and 4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20
L15	4	10 and 5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20
L16	19	10 and 6	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20
L17	3	10 and 7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:20
L18	50	11 12 13 14 15 16	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:21
L19	47	(adjust\$4 compensat\$6) with (phase jitter delay offset) with (TDM TDMA (time adj slot)) with (MUX multiplex\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36
L20	2184	(adjust\$4 compensat\$6) with (phase jitter delay offset) with (MUX multiplex\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:54
L21	12	20 and 1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36
L22	10	20 and 2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36
L23	12	20 and 3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36
L24	57	20 and 4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36

L25	14	20 and 5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36
L26	80	20 and 6	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:39
L27	14	20 and 7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:36
L28	166	20 with filter\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:49
L29	26	28 and (TDM (time adj slot) TDMA)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:47
L30	39	(adjust\$4 compensat\$6 synchroniz\$6) with (phase jitter delay offset) with (MUX multiplex\$4) with TDM	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 16:56
L31	63	(adjust\$4 compensat\$6 synchroniz\$6) with (phase jitter delay offset) with (MUX multiplex\$4) with select\$4 with (slot channel)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:07
L32	33183	delay with filter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:07
L33	659	phase with transition with filter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:11
L34	12	33 with multiplex\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:08
L35	0	33 same TDM	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:09
L36	7	33 and TDM	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:09

L37	2338	phase adj filter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:11
L38	0	37 with TDM	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:11
L39	33	37 with Multiplex\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:11
L40	12	(adjust\$4 compensat\$6) with (phase jitter delay) with (TDM TDMA (time adj slot)) with filter\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:21
L41	8	(compensat\$6) with (phase jitter delay) with difference with (TDM TDMA (time adj slot) (time adj division)) with filter\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:22
L42	49	(compensat\$6) with (phase jitter delay) with difference with (TDM TDMA (time adj slot) (time adj division))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:24
L43	382	(compensat\$6) with (phase jitter delay offset) with (TDM TDMA (time adj slot) (time adj division))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:24
L44	27	43 and 1	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L45	13	43 and 2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L46	9	43 and 3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L47	21	43 and 4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L48	10	43 and 5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25

L49	36	43 and 6	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L50	1	43 and 7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L51	81	44 45 46 47 48 49	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:25
L52	29	51 and filter\$4	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/21 17:26